

Two Days FDP

on
**Integrating
Artificial Intelligence
into Electrical Engineering**



 Venue: Seminar Hall,
EEE Department

 DATES
29 – 30 April 2026

 Time
9.30am to
04.00pm

HIGHLIGHTS OF THE FDP

-  Introduction to AI in Electrical Engineering
-  AI Applications in Core EE Areas
-  Industry Interaction & Expert Talks
-  Hands-on Training & Tools

WHO CAN ATTEND

-  Faculty Members
-  Researchers

SCAN QR FOR REGISTRATION



REGISTER NOW

RESOURCE PERSONS

Mr. Ramachandran P
Senior Technical Manager
NVIDIA



Mr. Wilson A
General Manager
TATA Elxsi





 **REGISTRATION FEE**
₹ 236 (Including GST)
For External and Internal Participants






PAYMENT DETAILS

Account Holder Name : BSACIST
Account Number : 165702000000786
IFSC Code : IOBA0001657
UPI ID : bsacist00786@IOB

CONVENERS

-  Dr. C. Tharini, Prof & Dean / SECS
-  Dr. S. Jennathu Beevi, Associate Professor & Head / EEE

CO-ORDINATORS

-  Dr. K. R. Shanmuga Vadivu, AP / EEE
-  Dr. P. Abirami, AP / EEE
-  Dr. R. Hannah Lalitha, AP / EEE

Dr. K.R. Shanmuga Vadivu 87545 87966
shanmugavadivu@crescent.education

Dr. P.Abirami 95665 86867
abirami.eee@crescent.education